



PATENT

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November 19, 2004 Carolyn L. Ross
Date Carolyn L. Ross

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Sion C. Quinlan and Tim J.
Bales

Attorney Docket No.: 500986.02

Serial No. : 10/057,205

Group Art Unit : 2829

Filed : January 25, 2002

Examiner : Scott B. Geyer

Confirmation No. : 1725

Date of Notice
of Allowance : October 6, 2004

Title : SEMICONDUCTOR PACKAGE ASSEMBLY AND METHOD FOR
ELECTRICALLY ISOLATING MODULES

Mail Stop Issue Fee
Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Sir:

In the Examiner's reasons for allowance, he states each independent claim is allowable because the prior art does not teach or suggest a particular element or elements recited in each of the claims. Specifically, independent claim 85 was amended to include the limitations of dependent claim 91, which the Examiner has stated is not disclosed nor suggested by the prior art of record. While applicant agrees with the Examiner's indication that the prior art of record does not teach or disclose the particular elements cited by the Examiner, the undersigned would

like to point out that each claim includes a combination of elements and it is the recited combination, which includes the elements cited by the Examiner, that is not disclosed nor suggested by the prior art of record.

While the combinations of elements recited in the allowed claims are allowable, the undersigned would like to point out that some or all of these individual elements may be broadened such that the resulting combination is still patentable. Applicant may elect to pursue such claims, or to pursue claims directed to other aspects of the present invention, through a continuation or reissue application, or through a reexamination proceeding.

Respectfully submitted,
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MS:clr

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